

LAP-C Pro(16064M)



General Free Protocols	1-WIRE, CAN 2.0B, IRDA, 3-WIRE, JTAG 2.0, PS/2, I2C (EEPROM 24LCS61/24LCS62), SSI Interface, MANCHESTER, MILLER, SPI Compatible (Atmel Memory), I2C, UART (RS-232C/422/485), SPI, 7-SEGMENT LED, USB 1.1, LCD1602, DIGITAL LOGIC, ARITHMETICAL LOGIC, MICROWIRE, I2C (EEPROM 24L), UP DOWN COUNTER, MICROWIRE (EEPROM 93C), MCU-51 DECODE, General Free Protocols
------------------------	---

Specifications

▶ Sampling rate

- . Internal (Timing)(Asynchronous) max. 1GHz
- . External (State)(Synchronous) max. 250 MHz

▶ Memory depth per channel

8ch/1Gb
16ch/512Mb
32ch/256Mb

▶ Total Memory

8Gb

▶ Trigger

- . 6 protocol triggers (hardware)(serial): I2C, I2S, SPI, SVID, UART (RS-232C/422/485), CAN 2.0B
- . Trigger types: Pattern/Edge/Pulse Width/Interval (Time)
- . Trigger channels: 40
- . Trigger delay: Yes
- . Trigger levels: 256
- . Trigger pass: 1-65535

▶ Pattern Generator

I2C, SPI, UART(RS-232C/422/485), CAN FD

▶ Data transmission

USB 3.0 (compatible with 2.0)

▶ Operating system

Windows (10/8.1/7)

▶ Special software features

- . Zooming and panning
- . Waveform and UI Customization
- . State list and Waveform view
- . Packet list
- . Waveform Navigator

- . Memory view
- . Statistic
- . File Comparison

▶ **Phase error**

<500 ps

▶ **Power supply**

USB bus-power (+5V)

▶ **Threshold Range**

-6V ~ +6V

▶ **Threshold Accuracy**

10mV/step

▶ **Dimensions**

126 x 95 x 25 mm

▶ **Certifications**

FCC/CE/WEEE/RoHS