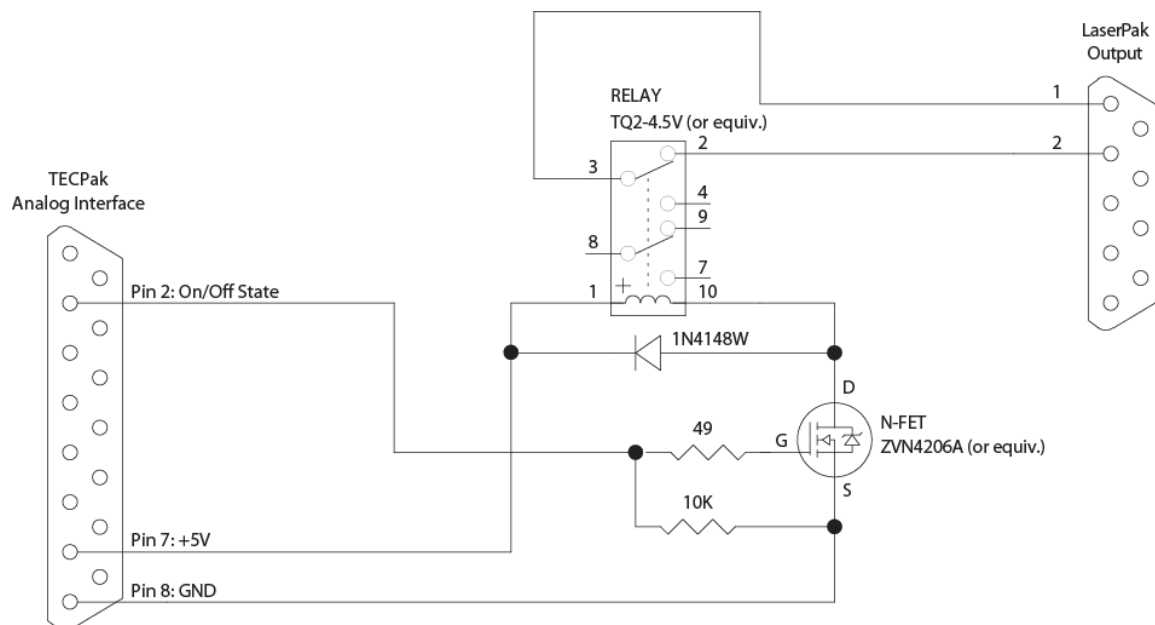# APPLICATION NOTE AN-001

Interlocking a LaserPak & TECPak with a Relay

July 24, 2014, Rev A

The following documents how to use the analog interface of the TECPak to control the interlock signal on the LaserPak to prevent operation of the laser unless the TECPak output is enabled.

Implementation

The following circuit uses a relay to provide an isolated contact closure for the interlock pins of the LaserPak's output connector.

There are many possible choices for the N-channel FET, but it must have the following capabilities: Vds of 5V or greater; current handling of at least 100mA; and a logic-level FET, as the gate voltage will only be 3.3V.

Likewise, there are many relay choices, but the coil voltage must be 4.5-5V, and draw less than 50mA when operating.