

MP1595A

40G SDH/SONET Analyzer
(Jitter version)

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MP1595A 40G SDH/SONET Analyzer Quick Start Guide (Jitter Version)

July 25th, 2007 (Version 3.2)

Anritsu Corporation
IP Network Measurement Division

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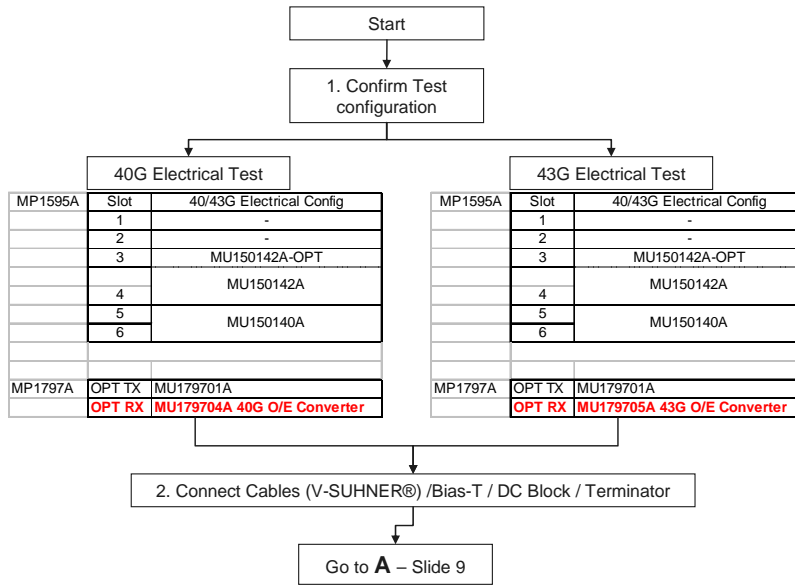
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MP1595A/MP1797A Configuration Flow-Chart



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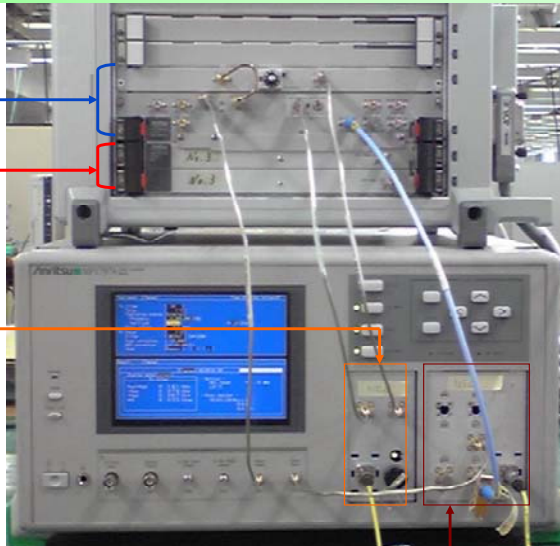
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1.0 MP1595A/MP1797A 40/43G Modules

MP1595A	Slot	40/43G Electrical Config
	1	-
	2	-
	3	MU150142A-OPT
	4	MU150142A
	5	MU150140A
	6	

MP1797A	OPT TX	MU179701A
	OPT RX	MU179705A 43G O/E Converter
		or MU179704A 40G O/E Converter



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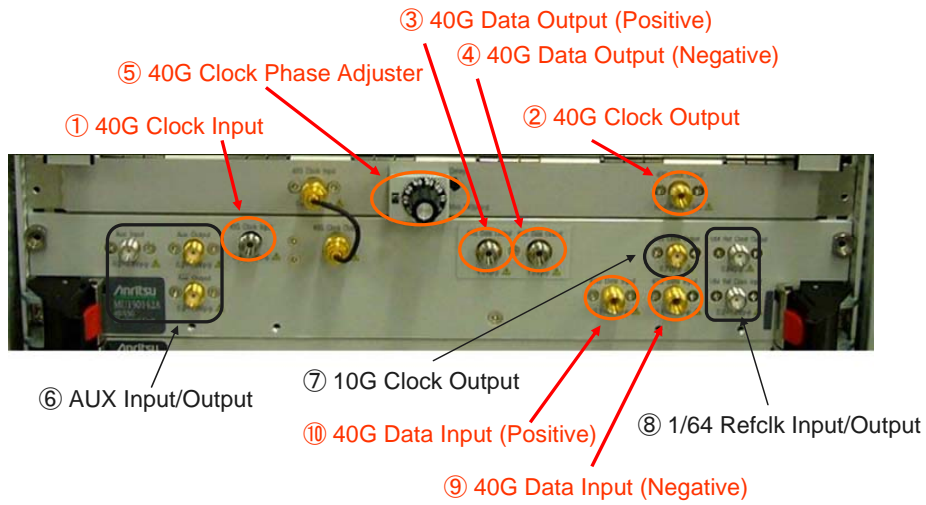
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1.1 Connector Locations and Names (MP1595A)

MU150142A : Electrical Unit



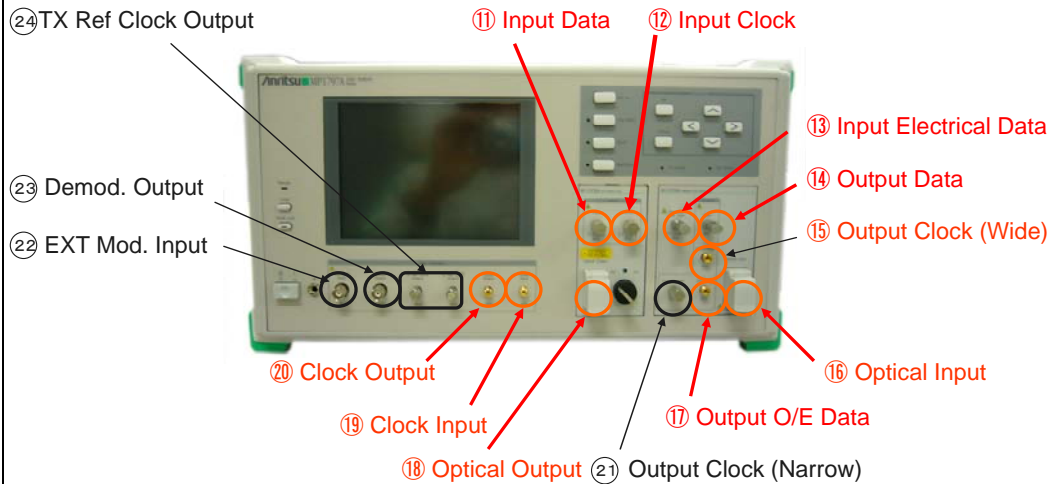
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1.2 Connector Locations and Names (MP1797A)



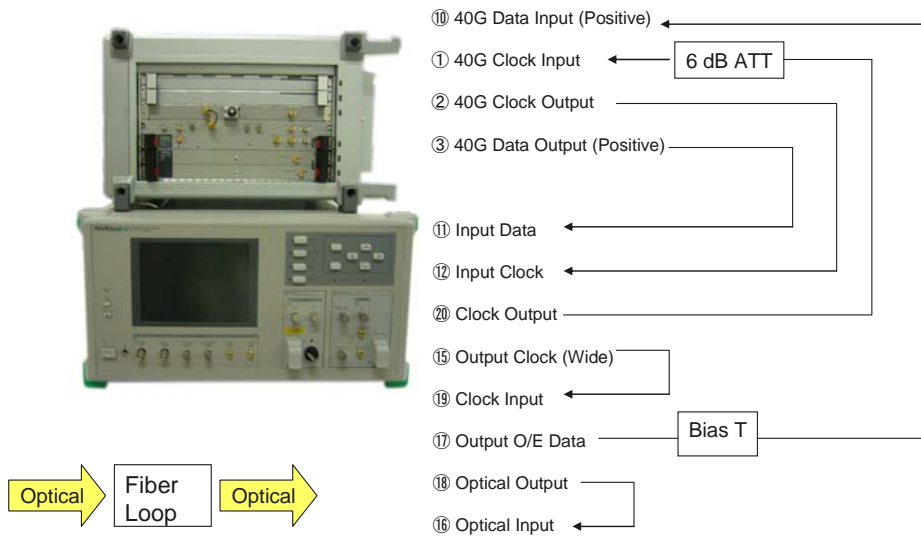
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1.3 Loopback for System Verification



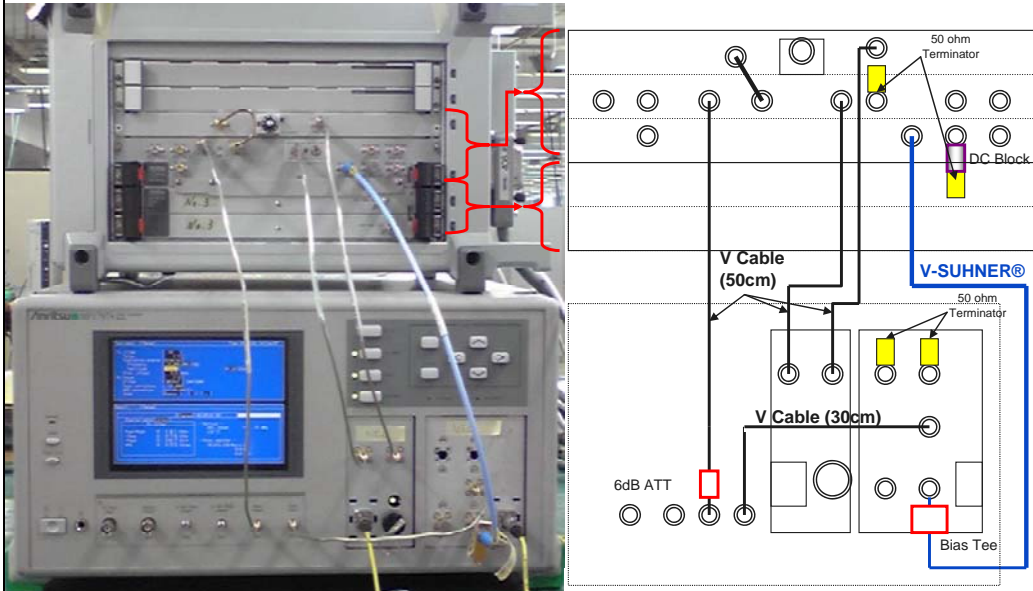
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1.4 MP1595A/MP1797A 40/43G Electrical Connections



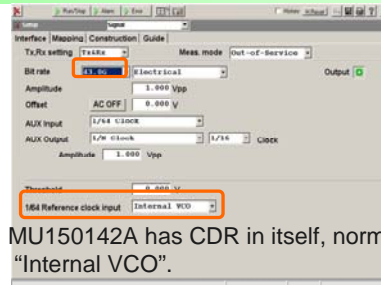
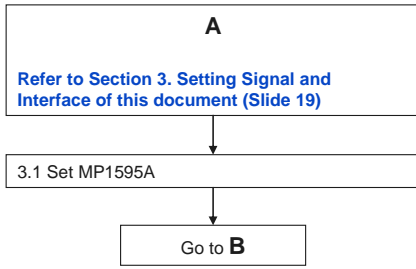
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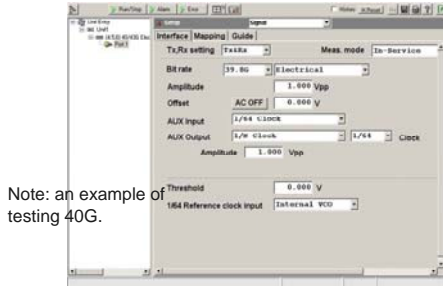
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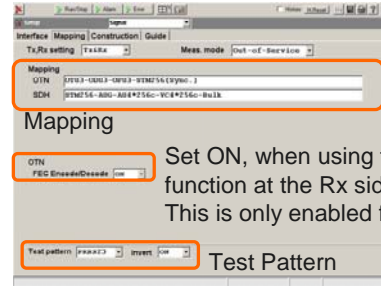
MP1595A/MP1797A Setup Flow-Chart



MU150142A has CDR in itself, normally select "Internal VCO".



Note: an example of testing 40G.



Mapping

Set ON, when using the FEC function at the Rx side. This is only enabled for 43G.

Test Pattern

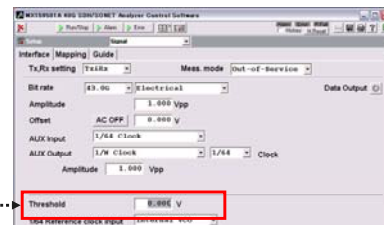
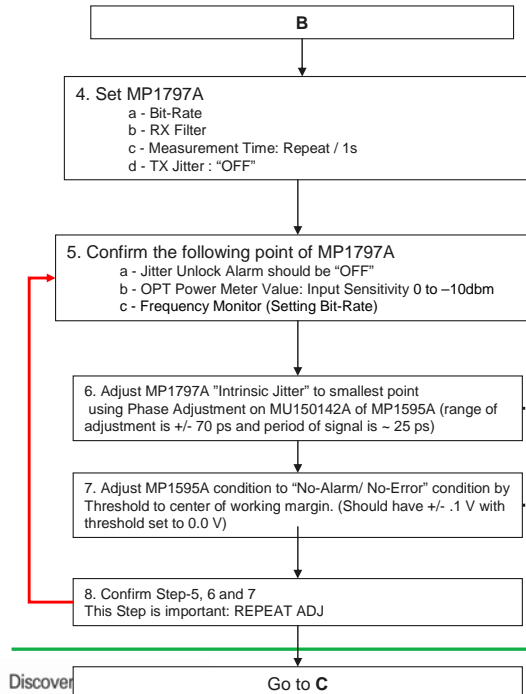
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MP1595A/MP1797A Setup Flow-Chart



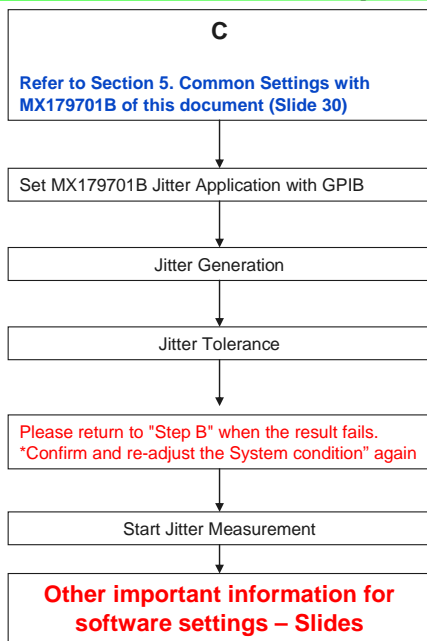
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MP1595A/MP1797A Setup Flow-Chart



Result | Jitter generation

Recommendation : G.783

Meas. time : 60s

Display type UIp-p

No	Bandwidth	UIp-p	Result	Judge
1	80kHz - 320MHz	0.300	0.106	OK
2	16MHz - 320MHz	0.100	0.093	OK

Result | Jitter tolerance

Current Freq. offset: 0.0ppm

No	Frequency(Hz)	Tolerance(UIp-p)	No	Frequency(Hz)	Tolerance(UIp-p)
1	10.0	-----	11	2,500,000.0	-----
2	40.0	-----	12	4,000,000.0	-----
3	160.0	-----	13	6,000,000.0	-----
4	640.0	-----	14	8,000,000.0	-----
5	2,500.0	-----	15	12,000,000.0	-----
6	10,000.0	-----	16	16,000,000.0	-----
7	40,000.0	-----	17	25,000,000.0	-----
8	160,000.0	-----	18	80,000,000.0	-----
9	640,000.0	-----	19	180,000,000.0	-----
10	1,600,000.0	-----	20	320,000,000.0	-----

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2.0 Pre-usage Notes

Setting-up Main Frame

- The MP1595A and MP1797A use internal cooling fans. Leave at least 10 cm around the back and sides to allow sufficient cooling air flows.



ALWAYS wear a wristband.

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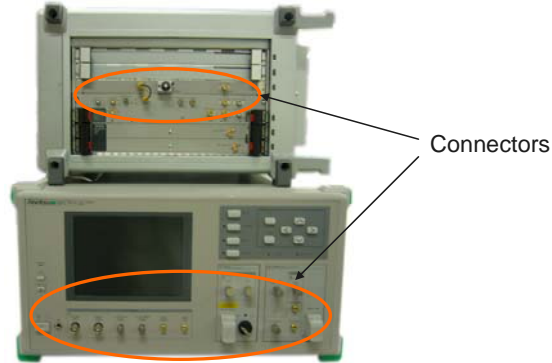
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2.1 Pre-usage Notes

✧ Setting-up Main Frame

- The MP1595A and MP1797A must be connected using cables to make measurements. Keep the cables as short as possible to prevent cable strain and make sure that the connectors on both units are oriented in the same direction as shown below.



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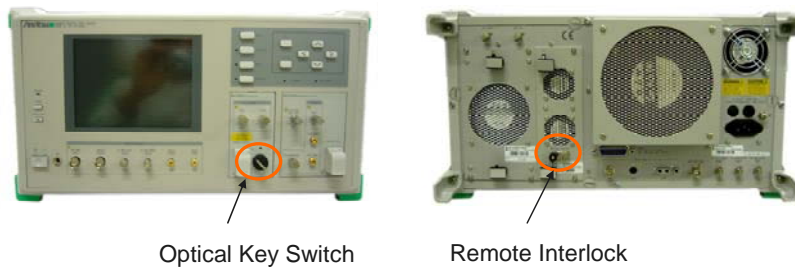
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2.2 Pre-usage Notes

✧ Checking Optical Output Key

- Insert the key into the optical key switch and Remote Interlock in the MP1797A E/O Module (MU179703A). Set the key switch to the ON position. If it is at the OFF position, the MU179703A will not output the optical signal.



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2.3 Power-on

✧ MP1595A Power Switches

- The MP1595A has two power switches—the main power switch and a switch for the standby status.

Setting power to ON

- (1) After checking that the main power switch is OFF, connect the power cord to the power connector.
- (2) Set the main power switch to ON.
- (2') To set the power to ON from the standby status, press the standby switch.

Setting power to OFF

- (1) Press the standby switch.
- (2) Check that the standby status has been entered and then switch off the main power switch.



Supply power at 100–120/200–240 Vac and 50–60 Hz to the MP1595A and MP1797A. Both units have voltage auto-switching.

2.4 Power-on

✧ MP1797A Power Switch

- The MP1797A has one main power switch.

Setting power to ON

- (1) After checking that the main power switch is OFF, connect the power cord to the power connector.
- (2) Set the main power switch to ON.

Setting power to OFF

- (2) Set the main power switch to OFF.



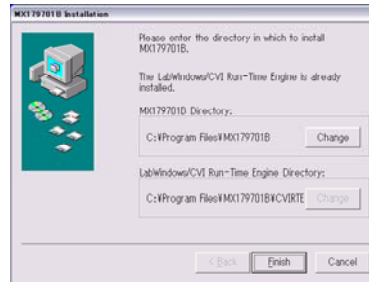
There is no special power-on/off sequence for the MP1595A and MP1797A. Switch either unit on or off in any order.

2.5 MX179701B Setup

✧ **Measurement is performed from a PC in which the MX179701B dedicated control software is installed.**

- (1) Copy the MX179701B folder on the installation CD-ROM to the PC hard disk.
- (2) Open the copied folder and click the setup.exe file.
- (3) Specify the destination for the install files if the default path is unsatisfactory for some reason and click the Finish button.

- (4) Installation starts automatically and is completed when the following dialog is displayed.



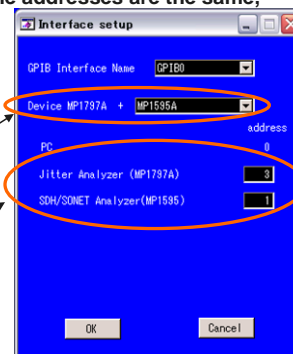
The OS of the PC controller should be Windows98 or later.

2.6 GPIB Setup

✧ **The MP1595A and MP1797A are controlled using the dedicated MX179701B control software.**

- (1) Connect the MP1595A and MP1797A to the PC in which the MX179701B software is installed using a GPIB cable.
- (2) Switch on the power of the MP1595A and MP1797A.
- (3) Check the GPIB address of the MP1595A and MP1797A. If the addresses are the same, change the address of one unit.
MP1595A: Selector → Setup Utility → Remote Control
MP1797A: Setup screen → System
- (4) Start the MX179701B software and launch the Interface Setup screen from File → Interface.
- (5) After changing the Device setting to MP1797A+MP1595A, check the GPIB addresses as described in step (3).

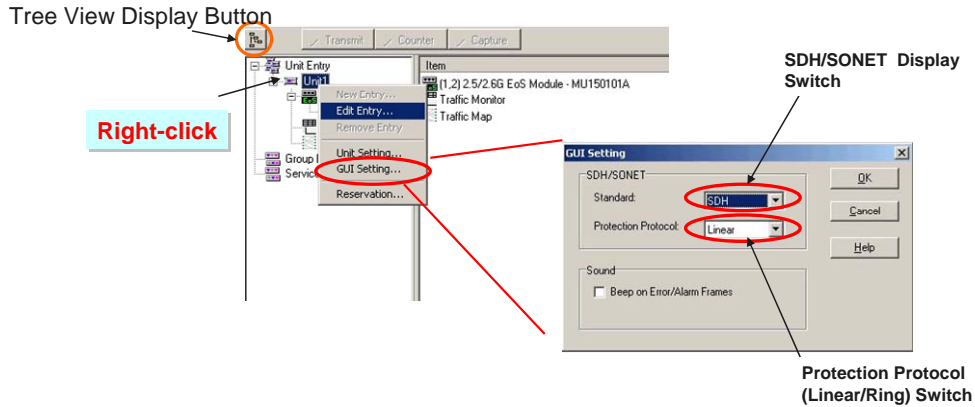
Device
GPIB Address



3.0 Setting Signal and Interface

✧ SDH/SONET display is switched at the MP1595A GUI setting window.

- Right-click Unit of Tree view and select GUI Setting... . Choose either SDH or SONET in the Standard: field and click OK.



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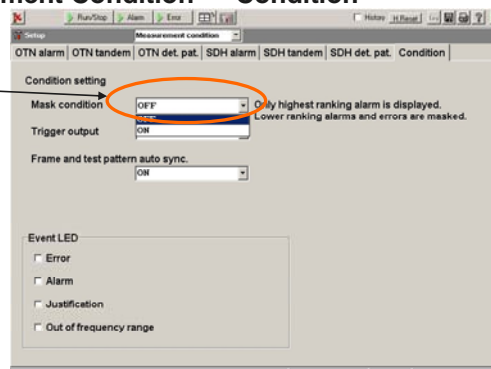
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3.1 Setting Signal and Interface

✧ At the MP1595A, it is possible to set masking of lower ranking errors and alarms to display only the highest ranking errors and alarms. At Jitter measurement, set masking to OFF.

Setup screen → Measurement Condition → Condition

Set to OFF.



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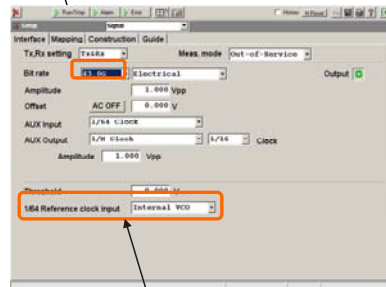
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3.2 Setting Signal and Interface

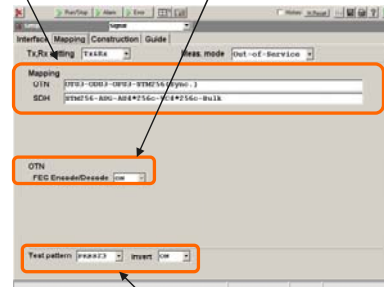
- ✧ **Set the Signal rate and Mapping used at measurement from the Setup screen.**

Rate Setting



Mapping Setting

Set ON, when using the FEC function at the Rx side. This is only enabled for 43G.



So MU150142A has CDR in itself, normally select "Internal VCO". When inputting a Clock to the ⑧ in Slide10 "1/64 Refclk Input/Output", select "Electrical 1/64".

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3.3 Setting Signal and Interface

- ✧ **About Test Pattern Setting**

- The Test Pattern complies with ITU-T O.150.

Test Pattern	Inverse/Non-inverse
PRBS15	Inverse
PRBS23	Inverse
PRBS31	inverse

- When Invert: OFF is set at the MP1595A, the above-specified patterns are sent. Since PRBS15/23/31 are defined as inverse by O.150, the actually sent pattern is a Negative PRBS pattern. Conversely, when Invert: ON is set, the pattern is double-inverse and a Positive PRBS pattern is sent.

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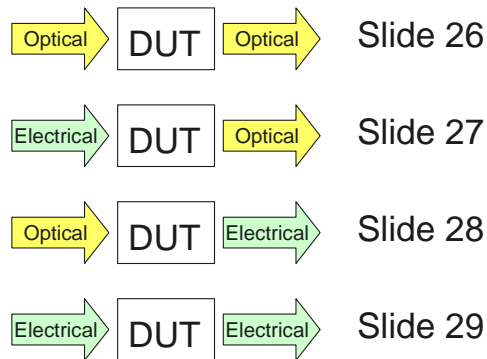
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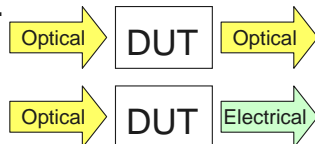
4.0 Connecting DUT

- ✧ There are four methods for connecting the measuring equipment and DUT as listed below. See the relevant slide for each connection method.



4.1 Adjust clock phase before connecting DUT

- ✧ When testing using the following setup, it is necessary to adjust the phase difference between the output clock and the data output from the MP1595A before the connection with the DUT.

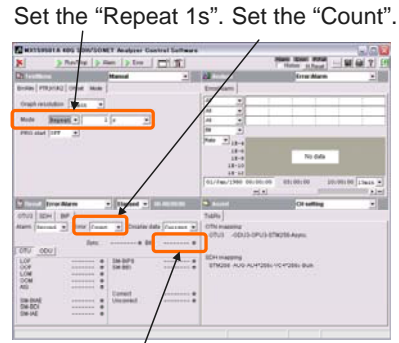


1. At the setup in Slide18, connect without an intermediate DUT.
2. When adjusting at 43G, set the FEC setting in Slide14 to OFF.
3. Set the ⑤ in slide10 "40G Clock phase adjuster" to 0.00.



4.2 Adjust clock phase before connecting DUT

4. On the MP1595A display, Set Test Menu -> Manual -> Mode -> "Repeat 1s", and Result -> Error/Alarm -> Error -> "Count"
5. See the number of error count and increase the adjuster value to which point has no error. -> the value is Min.
6. See the number of error count and continuously increase the adjuster value to which point starts to have some errors again. -> the value is Max.
7. Set the below value to the adjuster.
(Max + Min) / 2



With 0.2UI Jitter on 160MHz, the mid-point can be found easily.

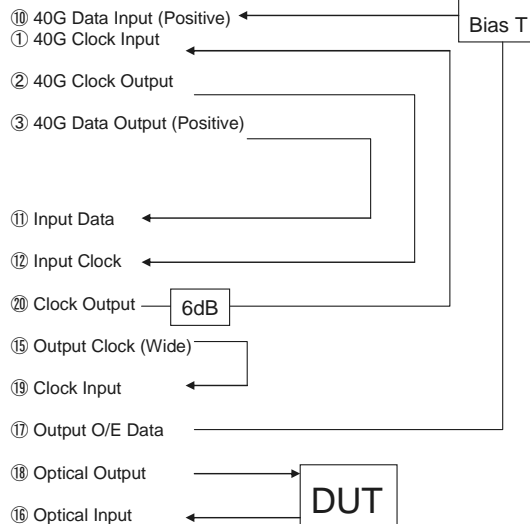
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4.3 Connecting DUT



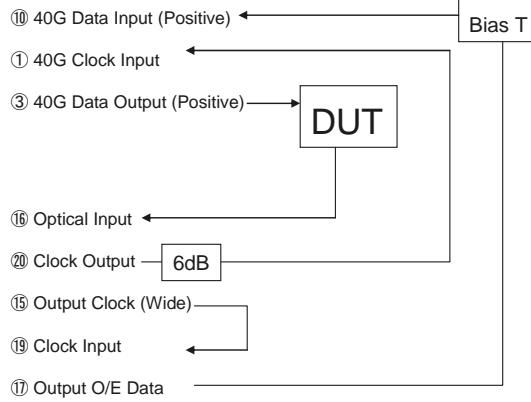
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4.4 Connecting DUT



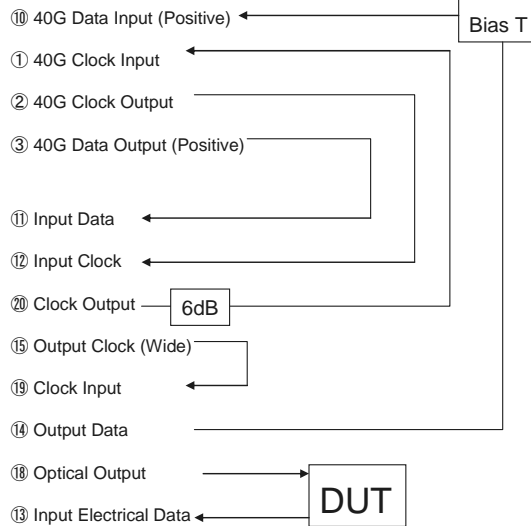
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4.5 Connecting DUT



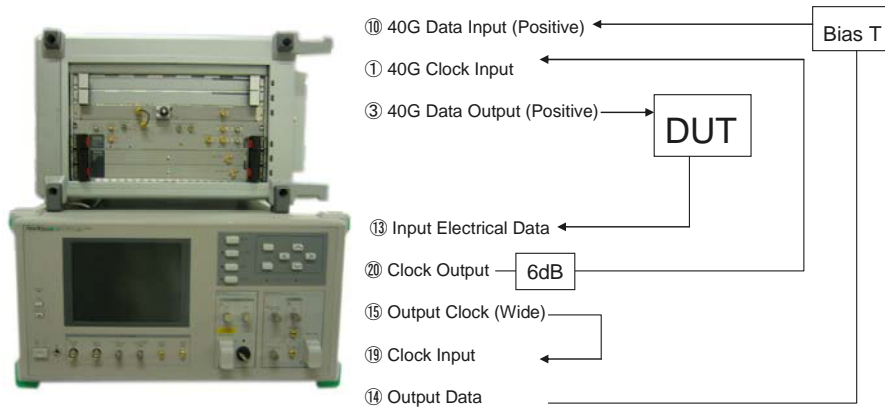
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4.6 Connecting DUT



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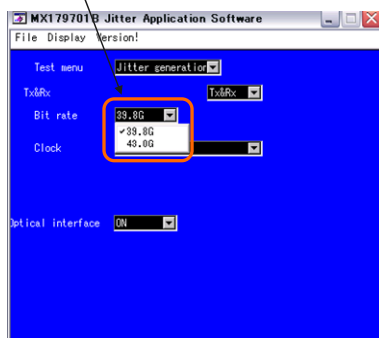
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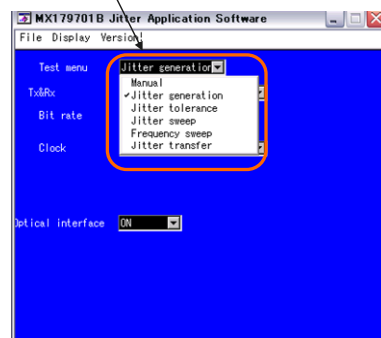
5. Common Settings with MX179701B

✦ Start the MX179701B and select the Rate setting and measurement items.

Rate Setting



Measurement Items



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6.0 Jitter Measurements

✧ There are three types of Jitter measurement as follows:

➤ Jitter Generation measurement

☑ This measures the residual or intrinsic Jitter at the DUT output

➤ Jitter Tolerance measurement

☑ This adds Jitter gradually to the DUT and measures the amount of Jitter at which the DUT can operate without generating errors.

➤ Jitter Transfer measurement

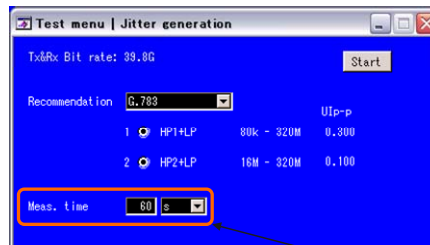
☑ This measures the degree to which Jitter is transferred to the DUT output side.

6.1 Jitter Generation Measurement

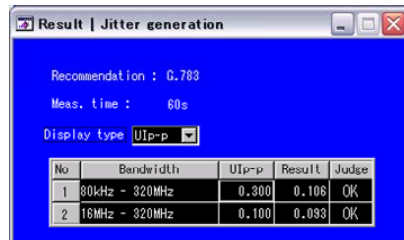
✧ Start the MX179701B and select Display → Test Menu and Result.

✧ After setting the standard and measurement time to be used for measurement at Test Menu, click the Start button to start measurement. The results are displayed on the Result screen.

Test Menu



Result

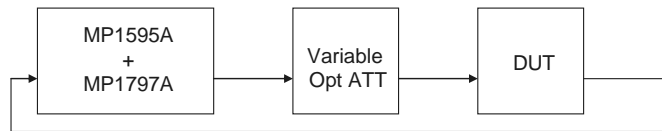


The Measurement time setting is defined 60 s.

6.2 Jitter Tolerance Measurement

✧ Perform 1dB penalty measurement before Jitter Tolerance measurement.* see note

1. Insert an optical attenuator between the measuring equipment and DUT.



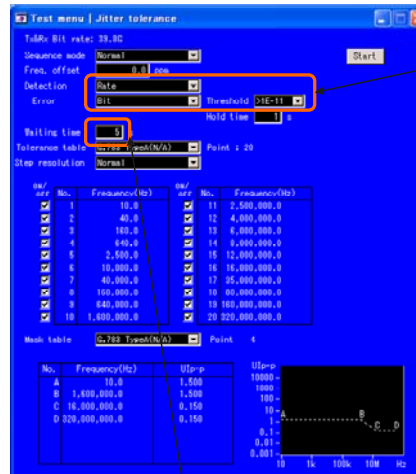
2. Attenuate the optical signal with the variable optical attenuator and set the attenuation amount (dB) so that the Error/Alarm rate monitored by the MP1595A becomes the threshold value used at Jitter Tolerance measurement. We recommend 1E-11 at Bit error for the threshold value.
3. Set the optical attenuator to a value 1 dB larger than the attenuation measured in step (2) and start the Jitter Tolerance measurement.

The 1dB penalty setting is not required when the DUT input is electrical.

6.3 Jitter Tolerance Measurement

✧ Start the MX179701B and select Display → Test Menu, Result and Analysis.

✧ After setting the standard, mask and Error/Alarm measurement targets, etc., click the Start button to start measurement.

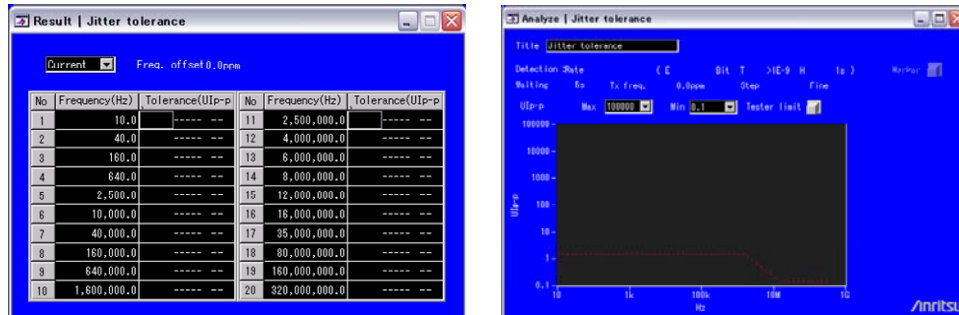


Measurement Target Error/Alarm Settings

The Waiting time setting is normally 5s.
If you know the DUT makes functional recovery less than 5s, you can set the less time to the waiting time.

6.4 Jitter Tolerance Measurement

- ✦ The measurement results are displayed on the Result and Analysis screens.



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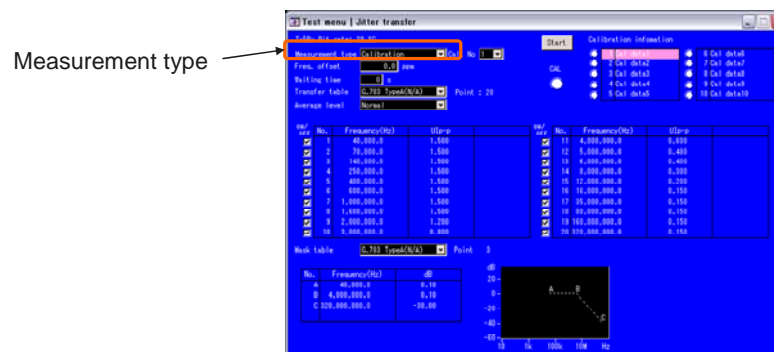
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6.5 Jitter Transfer Measurement

- ✦ Start the MX179701B and select Display → Test Menu, Result, and Analysis.
- ✦ Before performing Calibration, only Calibration can be selected for Measurement type. After Calibration, connect the DUT, switch Measurement type to Measurement and perform measurement.



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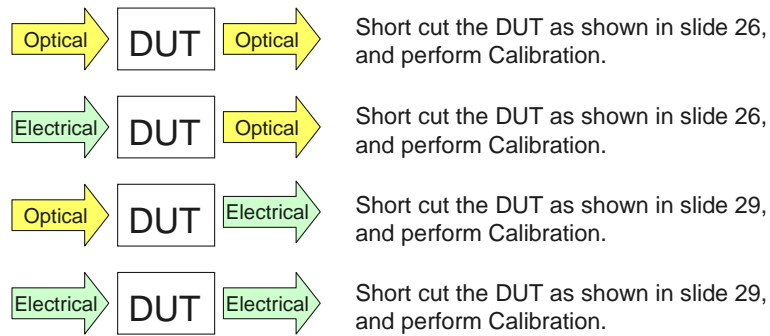
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6.6 Jitter Transfer Measurement

- ✦ After performing Calibration measurement with the measuring equipment looped-back, connect the DUT and perform the Jitter Transfer measurement.
- ✦ To perform Calibration for the receive side of the measuring equipment, make the following connections.



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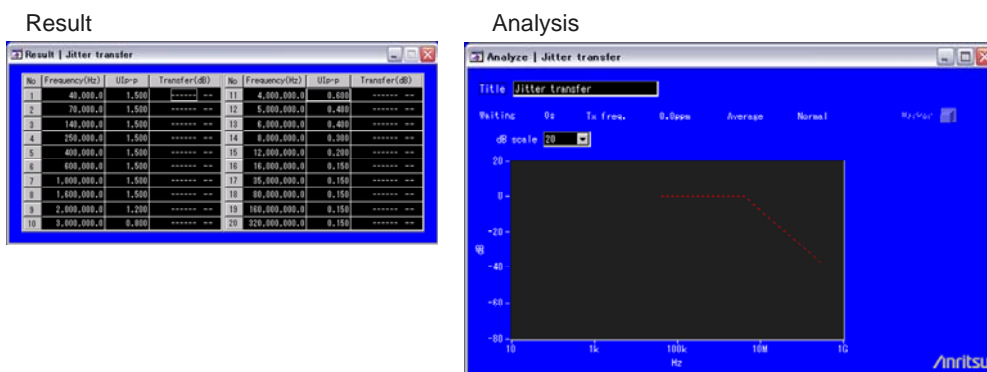
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6.7 Jitter Transfer Measurement

- ✦ The measurement results are displayed on the Results and Analysis screens.



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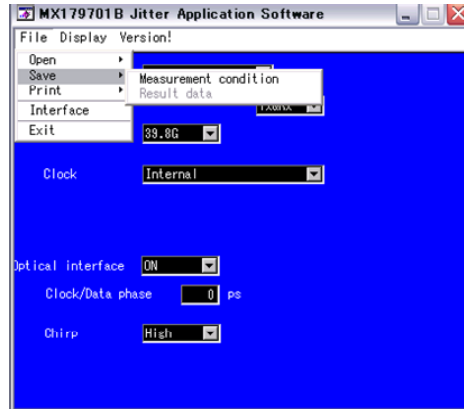
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7. Save/Load Method

- ✧ Save/Load is performed from the MX179701B File Menu.
- ✧ Save “Result data” as file type – “all.” This saves results in the MX179701B file format, as well as text and bmp (if applicable).



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Appendix

✧ MU150142A Connectors (1/2)

Item	Specification	Note
40G Clock Input		
Frequency	39.813120GHz, 43.018413GHz	
Amplitude	0.4 to 1.0Vp-p	
Termination	AC/50ohm	
Connector	V	
40G Data Input		Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Number of Input	2 (Data/XData)	
Input Amplitude	100 to 800mV pp	
Threshold	+0.1 to -0.1V, in 1mV steps at single-ended only	
Termination	AC/50ohm (connect DC Block externally)	
Connector	V	
40G Data Output		Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Number of Output	2 (Data/XData (Non-Independent))	
Amplitude	0.4 to 1.0Vp-p / 2mV Step	
Offset	-2.0 to +3.3Voh / 1mV Step	
Output	ON/OFF	
Connector	V	
40G Clock Output		
Frequency	39.813120GHz, 43.018413GHz	
Amplitude	1.0Vp-p(AC)±250mVp-p	
Termination	AC/50ohm	
Connector	V	

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Appendix

✧ MU150142A Connectors (2/2)

Item	Specification	Note
10G Clock Output		Trigger of Oscilloscope For MU150100A sync
Frequency	9.95328GHz, 10.75460339GHz	
Amplitude	0.7V _{p-p} (AC)±350mV _{p-p}	
Termination	AC/50ohm	
Connector	SMA	
1/64 Ref Clock Output		
Frequency	622.08MHz, 672.1627MHz	
Amplitude	0.8V _{p-p} (AC)±250mV _{p-p}	
Connector	SMA	
AUX Input		From MP1797A Reference Clock
Frequency	622.08MHz, 672.1627MHz	
Amplitude	200mV _{p-p} to 800mV _{p-p}	
Connector	SMA	
AUX Output		Reference Clock for Transponder/Serdes Oscilloscope trigger
Number of Output	2 (AUX/XAUX (Non-Independent))	
Variation	1/n Clock(n=16,32,64,128,256), Pattern Sync., AUX Input Through	
Amplitude	0.2 to 1.0V _{p-p} / 10mV Step	
Connector	SMA	

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Appendix

✧ MP1797A Connectors (1/5)

Item	Specification	Note
Clock Input		
Frequency	39.81312GHz +/- 100ppm, 43.01841GHz +/- 100ppm	
Level	+4dbm +/- 3db	
Termination	AC/50ohm	
Connector	V	
Clock Output		
Frequency	39.81312GHz, 43.01841GHz	
Level	+7dbm +/- 3db	
Termination	AC/50ohm	
Connector	V	
Electrical Clock Input		On the MU179703A
Bit Rate	25.0 to 43.5 Gbit/s	
Input Voltage	0.7 to 1.4 V(p-p)	
Waveform	Sine wave or rectangular wave	
Duty Cycle	45 to 55 %	
Waveform Distortion	10 % or less	
Connector	V	

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Appendix

✧ MP1797A Connectors (2/5)

Item	Specification	Note
Electrical Data Input		On the MU179703A
Bit Rate	25.0 to 43.5 Gbit/s	
Input Voltage	1.0 to 2.0 V(p-p)	
Code	NRZ	
Connector	V	
Optical Output		On the MU179703A
Modulator	LN	
Mean Launched Power	0dbm +/- 3db	
Extinction Rate	More than 10db	
Code	NRZ	
Connector	FC	
Center Wavelength	1530 to 1565 nm	
Optical Input		On the MU179704A
Bit Rate	39.81312 Gbit/s +/- 50ppm	Sensitivity
Wavelength	1530 to 1565 nm	0 to -8dbm : 10E-15 guaranteed
Sensitivity	0 to -10dbm	-8 to -10dbm : 10E-12 guaranteed
Overload	+3dbm	Non-frame PRBS31
Reflectance	Less than -27db	/ SDH VC4*256c-bulk(Scramble : ON)
Code	NRZ	
Connector	FC	

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Appendix

✧ MP1797A Connectors (3/5)

Item	Specification	Note
O/E Data Output		On the MU179704A
Bit Rate	39.81312 Gbit/s	Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Output Voltage	0.4 to 0.8 V(p-p) (Voh : 0V)	
Connector	V	
Narrow Clock Output		On the MU179704A
Bit Rate	39.81312 Gbit/s	
Output Voltage	0.7 to 1.3 V(p-p) (Voh : 0V)	
Connector	V	
Wide Clock Output		On the MU179704A
Bit Rate	39.81312 Gbit/s	
Output Voltage	0.7 to 1.3 V(p-p) (Voh : 0V)	
Connector	V	
Electrical Data Input		On the MU179704A
Bit Rate	39.81312 Gbit/s +/- 50ppm	Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Input Voltage	0.5 to 1.0 V(p-p)	
Code	NRZ	
Connector	V	
Data Output		On the MU179704A
Bit Rate	39.81312 Gbit/s +/- 50ppm	Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Output Voltage	0.4 to 0.8 V(p-p) (Voh : 0V)	
Connector	V	

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✧ MP1797A Connectors (4/5)

Item	Specification	Note
Optical Input		On the MU179705A
Bit Rate	43.01841 Gbit/s +/- 50ppm	Sensitivity
Wavelength	1530 to 1565 nm	0 to -8dbm : 10E-15 guaranteed
Sensitivity	0 to -10dbm	-8 to -10dbm : 10E-12 guaranteed
Overload	+3dbm	Non-frame PRBS31
Reflectance	Less than -27db	/ SDH VC4*256c-bulk(Scramble : ON)
Code	NRZ	
Connector	FC	

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Appendix

✧ MP1797A Connectors (5/5)

Item	Specification	Note
O/E Data Output		On the MU179705A
Bit Rate	43.01841 Gbit/s	Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Output Voltage	0.4 to 0.8 V(p-p) (Voh : 0V)	
Connector	V	
Narrow Clock Output		On the MU179705A
Bit Rate	43.01841 Gbit/s	
Output Voltage	0.7 to 1.3 V(p-p) (Voh : 0V)	
Connector	V	
Wide Clock Output		On the MU179705A
Bit Rate	43.01841 Gbit/s	
Output Voltage	0.7 to 1.3 V(p-p) (Voh : 0V)	
Connector	V	
Electrical Data Input		On the MU179705A
Bit Rate	43.01841 Gbit/s +/- 50ppm	Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Input Voltage	0.5 to 1.0 V(p-p)	
Code	NRZ	
Connector	V	
Data Output		On the MU179705A
Bit Rate	39.81312 Gbit/s	Fit a 50 Ω terminator when storing the equipment or not using it for measurement.
Output Voltage	0.4 to 0.8 V(p-p) (Voh : 0V)	
Connector	V	

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