18 – December 2017
Anritsu Corporation

MD1230/MP1590 Family Release Notes for Version 10.0

This Release Note covers the MD1230/MP1590 Family of products. This document contains the following Items.

Contents

1. Manuals

This upgrade contains the following manuals.

Model No.	Operation Manual	Edition
W3872AE	MD1230B7 Data Quality Analyzer Operation Manual	2.0
W1927AE	MD1230B Data Quality Analyzer Operation Manual	32.0
W3895AE	MP1590B8 Network Performance Tester Operation Manual	2.0
W2420AE	MP1590B Network Performance Tester Operation Manual	22.0
W1931AE	Ethernet Module Operation Manual	26.0
W3218AE	MU150110A Multirate Unit Operation Manual	7.0
W2427AE	MU150121A/22A/23A/34A 10/10.7G Optical Unit (Tx) 10/10.7G Optical Unit (Rx Narrow) 10/10.7G Optical Unit (Rx Wide) 10/10.7G Optical Unit (Tx Ex. Mod) Operation Manual	10.0
W2426AE	MU150125A 10/10.7G Jitter Unit Operation Manual	9.0

2. Version Information

This table shows the firmware versions for main units and modules. The version information can be confirmed either at the dialog displayed by clicking the [?] button at the top right of the Main Application screen or at the [Version] tab of the Setup Utility.

MP1590B should use the application before ver.10.00.xx(etc.9.06.21) because of not using Ver.10.00.xx Application.

Model Name	Versions
MD1230B7 Data Quality Analyzer **(Module Number is shown 'MD1230B' on the application)** **MX123001A Data Quality Analyzer Control Software**	**Version 10.00.02**
MD1230B Data Quality Analyzer	**Version 10.00.02**
MP1590B8 Network Performance Tester **(Module Number is shown 'MP1590B' on the application)** **MX159001B Network Performance Control Software**	**Version 10.00.01**
MP1590B Network Performance Tester	**Version 9.06.21**
MD1230B/B7 Data Quality Analyzer	boot 4.00.03 apl 9.06.10 FPGA/GPS 4.00.02 FPGA/DCS 4.00.00
MP1590B/B8 Network Performance Tester	boot 2.02.00 apl 9.06.11 CPU MAX FPGA 2.02.00 or 2.06.00 DCS MAX FPGA 2.02.00 or 2.04.00
MU120101A 10M/100M Ethernet Module	PCI 1.00.00 FPGA Tx/Rx 4.01.00 H8 3.00.01
MU120102A Gigabit Ethernet Module	PCI 1.00.00 FPGA Tx/Rx 8.01.01 H8 3.00.01
MU120103A 2.5G(1.31) Module MU120104A 2.5G(1.55) Module	PCI 1.00.00 FPGA/Tx SDH 2.00.06 FPGA/Tx PPP 3.00.08 FPGA/Rx SDH 2.00.03 FPGA/Rx PPP 3.00.06 H8 3.00.01
MU120103B 2.5G(1.31) Module MU120104B 2.5G(1.55) Module	PCI 1.00.00 FPGA/Tx SDH:G 3.00.04 FPGA/Tx PPP:G 3.00.07 FPGA/Rx SDH:G 3.00.12 FPGA/Rx PPP:G 3.00.12 FPGA/Tx SDH:P 3.00.07 FPGA/Tx PPP:P 3.00.09 FPGA/Rx SDH:P 3.00.08 FPGA/Rx PPP:P 3.00.18 H8 3.00.01
MU120105A 10G(1.31) Module MU120106A 10G(1.55) Module	PCI 1.00.00 FPGA/Tx SDH 3.00.04 FPGA/Tx PPP 3.00.08 FPGA/Rx SDH 3.00.02 FPGA/Rx PPP 3.00.08 H8 3.00.01
MU120111A 10/100M Ethernet Module	PCI 1.00.00 FPGA Tx/Rx 8.01.00 boot 2.01.03 apl 9.00.08

Model Name	Versions	
MU120112A Gigabit Ethernet Module	PCI FPGA Tx/Rx:D FPGA Tx/Rx:A boot apl	1.00.00 8.01.01 8.01.01 2.01.03 9.00.08
MU120118A 10 Gigabit Ethernet Module	PCI FPGA/Tx FPGA/Rx boot apl	1.00.00 8.01.00 9.00.00 3.04.01 9.00.08
MU120118B 10 Gigabit Ethernet Module MU120118C 10 Gigabit Ethernet Module	PCI FPGA/Tx FPGA/Rx boot apl	1.00.00 8.01.00 9.06.01 3.04.01 9.00.08
MU120119A OC-3/12 STM-1/4 Module (1310 nm) MU120120A OC-3 STM-1 Module (1310 nm)	PCI FPGA Tx/Rx boot apl	1.00.00 2.02.08 2.02.01 2.02.36
MU120121A 10/100/1000M Ethernet Module MU120122A Gigabit Ethernet Module	PCI FPGA Tx/Rx FPGA PPPoE Tx/Rx boot apl apl PPPoE	1.00.00 9.04.01 1.00.08 4.00.02 9.00.08 1.00.07
MU120131A 10/100/1000M Ethernet Module	PCI FPGA Tx/Rx boot apl	1.00.00 9.06.02 7.00.01 9.00.08
MU120132A Gigabit Ethernet Module	PCI FPGA Tx/Rx boot apl	1.00.00 9.06.02 7.00.01 9.00.08
MU120138A 10 Gigabit Ethernet Module	PCI FPGA Tx/Rx boot apl	1.00.00 9.06.05 9.00.00 9.00.08
MU150100A 10/10.7G Unit	 MAX FPGA	3.11.00 1.00.00
MU150101A 2.5/2.6G EoS Unit	 MAX FPGA H8	3.20.00 2.00.00 3.00.01
MU150110A Multirate Unit	 MAX FPGA apl Rx	1.20.00 1.00.00 1.00.43
MU150121A 10/10.7G Optical Unit (TX) MU150121B 10/10.7G Optical/Electrical Unit (TX) MU150122A 10/10.7G Optical Unit (RX NARROW)	MAX FPGA	1.00.00 1.00.00
MU150123A 10/10.7G Optical Unit (RX WIDE)	 MAX FPGA	1.00.00 1.01.00
MU150123B 10/10.7G Optical/Electrical Unit (RX WIDE) MU150124A 10.3G Optical Unit(RX WIDE) MU150124B 10.3G Optical/Electrical Unit (RX WIDE) MU150134A 10/10.7G Optical Unit (TX EX. MOD)	MAX FPGA	1.00.00 1.00.00
MU150125A 10/10.7G Jitter Unit	 MAX FPGA	2.00.00 1.00.00
MU150135A 10/10.7G Optical Unit(XFP)	 MAX FPGA	1.04.00 1.00.00

3. New added functions

This release adds the following new functions:

- Software installer compatible with MD1230B7 *1
- Software installer compatible with MP1590B8 *2

*1:MD1230B7 needs to be installed version 10.00.00 or later.
*2:MP1590B8 needs to be installed version 10.00.00 or later.

4. Specification Changes and Bug Fixes

This section lists the corrections and changes implemented in this release. * For previous changes, please contact our sales staff.

Model Name	Change
MD1230B7 Data Quality Analyzer	➢ MD1230B7 needs to be installed ver.10.00.00 or later. MD1230B7 supports only MU120131A/132A/138A module. In detail information, please read 'MD1230B7 Data Quality Analyzer Operation Manual'.
MP1590B8 Network Performance Tester	➢ MP1590B8 needs to be installed ver.10.00.00 or later. MP1590B8 supports only MU150110A/121A/123A/125A module. In detail information, please read 'MP1590B8 Network Performance Tester Operation Manual'.
MD1230B7 Data Quality Analyzer MP1590B8 Network Performance Tester	➢ [CM1130:1341] Fixed the bug that sometimes front panel keys and LEDs don't work (version 10.00.01 and later)
MD1230B/MD1230B7 Data Quality Analyzer	➢ [CM1130: 1342] Fixed the bug that the number of transmitting frames do not correct when enabling VLAN on RFC2544 automatic test. (version 10.00.02 and later)

5. Known Problems

Model Name	Known Bug Contents
MD1230B/MD1230B7 Data Quality Analyzer MP1590B/MP1590B8 Network Performance Tester MX123001A MX159001B Control Software	➢ [CM870: 0907] When Rate Counter is selected at the counter function graph display and the Resolution is other than 1 s, the value becomes the total value. ➢ [CM1130:0935] The Group function remote command does not operate correctly when displaying Group Counter at the screen. Recovery does not require selecting a different group from the displayed group using the remote command. To use Group function remote commands, either display the same group at the screen or move to a non-Group screen. ➢ [CM1617:0027] When DCS MAX_FPGA Version of MP1590B (MP1591A) is Ver2.02 (Ver6.06), Drop function of PDH 45M and 34M can not measure correctly. Please contact Anritsu service representative. ➢ [CM1130:1273] MP1590B: When only the single MU120118B/118C

	is inserted, the Tx Stream sent a 100% rate becomes 100.01%. This is caused by the malfunctioning variable Tx clock function. If this is the case, contact Anritsu service representative.
Ethernet Modules MU120101A MU120111A 10/100M Ethernet Module MU120102A MU120112A MU120122A MU120132A Gigabit Ethernet Module MU120121A MU120131A 10/100/1000M Ethernet Module MU120118A MU120118B MU120118C 10 Gigabit Ethernet Module	➢ [CM1130:1225] MU120121A/122A: In 10M Half, link down may happen when receiving the back pressure that does not fit the following conditions: ・ Back pressure pattern: 55 55 55 55 55 55 55 D5 xx xx (xx indicates JAM pattern) ・ Minimum gap: 12 byte ➢ [CM1130:0784] MU120102A/112A/118A/118B/118C: Using the Tx Stream function, when incrementing the TCP/UDP Port Number and Sequence Number (Data Field 1 SN, Test Frame SN, Programmable Header Pattern SN) with one setting stream (not multiple streams), the post-Jump value is not returned to the default value. ➢ [CM1130:0784] MU120118A/118B/118C: Using the Tx Stream function, when incrementing DA/SA of the MAC/IPv4/IPv6 and VLAN ID with one setting stream (not multiple streams), the post-Jump value is not returned to the default value. ➢ [CM1130:0948] MU120118A/118B/118C: When a Pause frame is received continuously, a Line Error occurs when sending and receiving at both ports. ➢ [CM1130:0886] MU120121A/122A: Using the Tx Stream function, when specifying UDP/IPV6 at the Protocol setting and Sequence number in Data Field, sometimes the Sequence Number becomes fixed to 0 depending on the Offset setting. ➢ [CM1130:1017] MU120118A/118B/118C: When a gapless frame is started following the LFS pattern, the frame header is not detected. ➢ [CM1130:1008] MU120118A/118B/118C: When a trigger is set at the [Latency is out of range] condition, it seems like there is a trigger two or three frames after the frame matching the conditions. ➢ [CM1130:0956] MU120118A/118B/118C: When flow control is enabled and Pause frames are received continuously, the Send button displays [Stopping] when frames are not being sent even when the TX Stream function is starting.
MU120103B MU120104B 2.5G Module MU150100A 10/10.7G Unit MU150101A 2.5/2.6G Eos Unit	➢ [CM1130: 0175] MU120103B/104B, MU150101A: When Port Setting - Scramble / Descramble core Header or Scramble / Descramble Payload Area setting OFF to ON, sometimes causing GFP error. ➢ [CM1130:0205] "0: Execution error" message sometimes displayed when switching from EoS mode to SDH/SONET/OTN mode ➢ [CM1130: 0274] MU150101A: Stream data is not sometimes sent normally, when receiving Ping/ARP packets in sending stream ➢ [CM1130:0601] MU150101A EoS mode: The printed "LCAS State" value is incorrect when MP1590B prints the Path Monitor Data. ➢ [CM1130: 0924] MU150100A: It has the possibility to occur "not error-free condition" infrequently encountered when changing bit-rate from 9953M to 10.7G. In that case, the condition recover to normal when setting bit-rate again from 10.7G to 9953M to 10.7G.

6. Usage Notes

Read the following terms and conditions before using the MD1230/MP1590 Family.

Model Name	Description
MD1230B7 Data Quality Analyzer	➢ MD1230B7 needs to be installed ver.10.00.00 or later. ➢ MD1230B7 supports only MU120131A/132A/138A module.
MP1590B8 Network Performance Tester	➢ MP1590B8 needs to be installed ver.10.00.00 or later. ➢ MP1590B8 supports only MU150110A/121A/123A/125A module.
MD1230B/MD1230B7 Data Quality Analyzer MP1590B/MP1590B8 Network Performance Tester MX123001A MX159001B Control Software	➢ [CM1130:0123] It is not possible to Copy & Paste Tx Stream settings straddling Unit. ➢ [CM870:0374] The stream setting is cleared when the unit module composition is changed. ➢ [CM563:0089], [CM620:0448] The capture function can display up to 64 Kbytes of characters. Characters exceeding this limit are not decoded or displayed at the decode screen. ➢ [CM620:0305] The error items displayed in the Capture Status do not include PRBS Bit Error and Sequence Error. ➢ [CM620:0244] The capture conditions are different between the Ethernet Module and POS Module when setting both the Trigger and Filter. The Ethernet Module captures both the Trigger Frames and Filter Frames. The POS Module captures only Filter Frames. ➢ [CM849:0115] The settable number of streams changes with the frame length. For details, see the operation manual. ➢ [CM1130:0018] When the capture filter conditions are mismatched and a longer Frame than the following frames is received, the size of the captured data is smaller than the built-in memory capacity. MU120101A/111A: 1000 byte MU120102A: 920 byte MU120112A/121A/122A: 1948 byte MU120118A/118B/118C: 3328 bye MU120103A/104A/105A/106A/103B/104B: 1266 byte MU120119A/120A: 400 byte MU120131A/132A: 8064 byte ➢ [CM1130:0206] Remote Command and Save Load cannot be performed while the Log Function is operating. ➢ [CM1130:0479] The RFC2544 Reset test cannot be performed between different units. ➢ [CM1130:0612] The OSPF function does not operate correctly when the same Router ID is set for multiple virtual routers. ➢ [CM1130:0740] When a large amount of data is captured at combined use with Ethereal/Wireshark, errors may occur due to Ethereal/Wireshark limitations. ➢ [CM1130:0931] When performing loopback sending in the Address Swap mode, Preamble information is not saved. As a result, the output frame Preamble pattern 55(h) may be different from the input frame pattern. ➢ [CM1130:0939] The Unit Alarm LED lights when the Alarm selected by the [Display Option] setting occurs. Moreover, the Port icon displays the status of alarms that are not selected by the [Display Option] setting. ➢ [CM1130:0897] MD1230A/MD1231A/MD1230B/MP1590B: When

Model Name	Description
	the Windows screensaver is set, faults may occur at long-term operation. The default shipping screensaver configuration is (None). Do not change this default setting. ➢ [CM1130:0937] The History LED off conditions are changed in Version 9.0. ➢ [CM1130:0734] When outputting the Port Setting report, only the setting contents selected at the Physical IF tab are output. ➢ [CM1130:0891] When outputting the report for the Capture results at the Group screen, if there is a difference in the number of frames captured at each port, sometimes the reported frame count is not in accordance with the specification. ➢ [CM1130:0903] MU120121A/122A/131A/132A/138A: The Flap Setting/Clock Offset Setting is not reported for unreserved ports. ➢ [CM1130:0903] MU120131A/132A/138A: The Clock Offset value is not reported when not all ports are reserved. ➢ [CM1130:0903] MU120131A/132A/138A: The Flap Setting at the Link Flap Start condition is not reported. ➢ [CM1130:0903] MU120118A/118B/118C: Sometimes the Clock setting report is not output.
Ethernet Modules MU120101A MU120111A 10/100M Ethernet Module MU120102A MU120112A MU120122A MU120132A Gigabit Ethernet Module MU120121A MU120131A 10/100/1000M Ethernet Module MU120118A MU120118B MU120118C 10 Gigabit Ethernet Module	➢ [CM1600:0466] When the ARP Reply setting at the Port Setting screen is set to Reply to all ARP Request, the network connection using multiple ports becomes abnormal because of an attempted response to all ARP Requests. Ensure that you understand how to use Reply to all ARP Request correctly. Normally, use Reply to this Port. ➢ [CM1600:0466] When the MAC Address setting of the This Port screen is set to multicast (LSB of header byte is 1), the send source addresses of the frames sent by the Port Protocol Emulation function all become multicast and operation of the connected network becomes abnormal due to an attempt to set the destination of frames responding to these packets to all multicast. Take care about this point. ➢ [CM1130:0244],[CM1130:0691] MU120102A/112A/122A(SFP)/132A: When the Tx Stream set frame length or gap length is an odd number, the actually sent ISG is 1 byte shorter than the set ISG. As a result, the actual send rate exceeds the set rate. ➢ [CM1262:0419],[ADE968:0038] MU120102A/112A/122A(SFP)/132A: When the Tx Stream function Frame setting is an odd value or Random/Increment, the minimum value of the IFG/IBG/ISG setting becomes 9 (bytes). ➢ When using this module for latency measurement in combination with MU120101A, select "MD1230A Test Frame for MU120101A" at the other module. ➢ [CM870: 1148] MU120101A/111A/121A/122A: When the cable is disconnected, the value of the transmission rate counter becomes abnormal. ➢ [CM488:0383] MU120118A/118B/118C: It inserts bit errors only in Lane3 in the individual mode with Option-13 Unframe BER measurement. Set the Type to Bit all (Lane0), Bit all (Lane1), Bit all (Lane2), and Bit all (Lane3) successively, to perform each measurement.

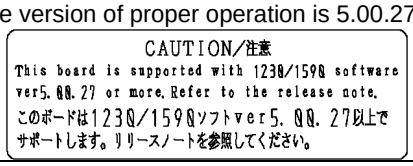
Model Name	Description
	➢ [CM1130:0302] When there is a mismatch in the In and Out Link conditions (10M/100M/1000M, Full/Half) in the Through mode, communications cannot be performed normally. ➢ [CM1130:0302] When using the Through mode, Link Up must be performed first in the Normal mode before changing to the Through mode. ➢ [CM1451:0070] MU120121A/122A/131A: When the Link Speed is 1000 Mbps, 1byte of the header Tx Stream setting Preamble (variable) field cannot be edited. 55 (hex) is always sent. ➢ [CM1451:0069] MU120121A/122A/131A: When the Link Speed is 10 Mbps, the Preamble Capture/Counter Function does not operate normally. ➢ [CM1130:0125] MU120118A/118B/118C: When PCS Type = WAN (WAN-PHY) and the ISG byte count is set to a decimal value, the rate at the setting screen is not sent. ➢ [CM1130:0459] MU120121A/122A(RJ-45)/131A: Sometimes Link Down occurs when output pattern of DUT at back pressure is only JAM. Output pattern must be Preamble + SFD + JAM. ➢ [CM1130:0485] Even when the cable is connected, Link Up cannot be established while frames are being sent from the DUT when Line Speed 10M and Auto negotiation Off are set at the RJ-45 port of the MU120121A/122A. ➢ [CM1130:0989], [CM1130:0888] MU120131A/132A: The Test Frame count when Type is Flow ID has been removed from the Sequence Error count. In addition, the Test Frame count when Type is PRBS has been removed from the multi-flow counter Sequence Error count. As a result of this fix, Flow ID test frames are counted by the multi-flow counter Sequence Error and other types of test frame are counted by the Sequence Error counter (not multi-flow counter). (Versions 9.0 or later) ➢ [CM1130:0800] When the Port Setting Preamble setting is On and the Tx Stream Protocol is MAC Control Frame, although the Frame View Decode display is abnormal, the data is actually sent according to the setting contents. To confirm the sent contents with the Decode display, set the Preamble setting to Off. ➢ [CM1130:0799] Using the Tx Stream function, when the Protocol setting is set to MAC Control Frame, the Preamble setting is not displayed but the setting is enabled. (Operation is in accordance with the setting contents before the display disappeared.) ➢ [CM1130:0818] When Port Setting Mapping is set to Unframed, set the Flow Control setting to Off. ➢ [CM1130:0841] The 1000BASE-T GBIC is only supported by the MU120112A, it cannot be inserted at the MU120102A. In addition, note the following precautions when using with the MU120112A. - Use the 1000BASE-T GBIC with the G0124A accessory. Operation is not assured with other parts. - Set the Auto Negotiation setting to Off. - The MII Register setting screen cannot be used. - The Self Test Flow Control test cannot be performed. ➢ [CM1130:1034] When Oversize is specified at Tx Stream Error Insertion, a 1519-byte frame is sent. As a result, when setting the Maximum Frame Size for Port Setting to a value larger than 1519,

Model Name	Description
	an Oversize Error is not counted even when sending this stream. ➢ [CM1130:0997] MU120118C: When confirming the MU120118C version at the Version tab of Setup Utility, the number for the larger of the two slots is displayed, but the smaller slot number is specified and used with the MX123001A.
MU120103A, MU120104A, MU120103B, MU120104B 2.5G Module MU120105A, MU120106A 10G Module MU120119A OC-3/12 STM-1/4 Module (1310 nm) MU120120A OC-3/STM-1 Module (1310 nm)	➢ [CM870:0253] The displayed Latency results may be disabled (-). ➢ [CM620:0309] When PPP, LEX, or LAPS is selected for mapping, the dummy payload excludes flags. ➢ [CM849:0206],[CM1804:0008] MU120103B/104B, MU150101A: The frame that is received immediately after the Mapping is changed to GFP has a tHEC error. This happens because the first received frame has insufficient descramble data when scrambled in frame units by GFP. ➢ [CM563:0191],[CM563:0193] MU120119A/120A: When "Loopback" is set at the Port Settings screen, Frame is also sent externally. ➢ [CM1130:0908] When specifying [Test Frame for MU120101A] at Data Field of Tx Stream and setting Frame Length to Auto, operation is not in accordance with the settings even if Offset is set to a value larger than 0.
Option 10 RFC2889 Benchmarking Test	➢ [CM1130:1041] The Learning Frame is sent only once at the measurement start binary even for time-consuming measurements, such as RFC2889 Automatic Test - Throughput. As a result, at one-way tests such as Forward Pressure and Maximum Forwarding Rate, the receiving-side port may be erased from the address table during measurement, possibly preventing output of correct results. ➢ [CM1130:0993] At the Address Caching Capacity Test of RFC2889 Automatic Test, the sending rate for the test frame (sent from T Port to L Port) is the same as the sending rate (Address Learning Rate) for the Learning Frame (sent from L Port to T Port). Since the default Address Learning Rate is a low 50 fps, the Age Time until sending is completed overflows if there are many addresses, and sometimes correct measurement is impossible. ➢ [CM1130:0998] Sometimes [Forward pressure detected] is evaluated by mistake at RFC2889 Automatic Test - Forward Pressure and Maximum Forwarding Rate (with devices having send and receive buffers). ➢ [CM1130:1004] Even when Address per Port is set to a larger value than 1, operation is not performed according to the setting at RFC2889 Automatic Test - Congestion Control. ➢ [CM1130:1006] Although Addresses per Port is included in the RFC2889 Automatic Test - Broadcast Frame Forwarding and Latency setting items, it has no meaning even if set.
Option 01/02/03 Option 07/09/10 (MX123001A) RS-232C/GPIB/Ethernet Control	➢ [CM488:0120],[CM488:0501] When remote control is released while executing an automatic test using a remote command, it is not possible to measure correctly. ➢ [CM620:0422] The contents of the Programmable pattern setting for Data Field 1 display All 0 during sending. ➢ [CM1130:0858] When capturing graph data using the COUNter:GRAPh:DATA? command, the graph must be displayed

Model Name	Description
	once on the screen. From Version 9.0, this restriction applies only to the multiflow counter but not to other counters.

7. Upgrade Notes

Model Name	Description
MD1230/MP1590 Family	➢ Read the “Upgrading Software Manual” on the Upgrade CD-ROM before updating the MD1230 Family software. ➢ [CM1130:0483] Normal operation is not assured when a version check error occurs. Upgrade the firmware using the Download function of the Setup Utility. For details, refer to the Upgrade Manual. ➢ When the software upgrade is done, the configuration file might not be able to be read. In this case, convert the configuration file using "Setup File Converter". ➢ [CM1804:0001] Changed supported Ethereal version to 0.10.13 (Ver.8.2 or later)
MD1230B/MD1230B7 Data Quality Analyzer	➢ [CM1130:0975] The MD1230B firmware cannot be downgraded to versions earlier than Version 4.1. Installing Version 4.0 or earlier in the MD1230B may prevent normal start-up. ➢ MD1230B: When the software version is earlier than 4.01.19 (4.0106, 4.0111, and 4.01.18, etc.), the software upgrade cannot be done. Please contact our sales representatives. ➢ [CM1130:0197] MD1230B: Sometimes a Kernel.exe Application Error occurs after software install but there is no actual operation problem. Please contact our sales representative if this problem happens. ➢ [CM1130:1300] The product key for software installer is not necessary on version 9.06.10 or later ➢ The MD1230B7 cannot be downgraded to versions earlier than Version 10.00.00 Installing Version 9.x or earlier in the MD1230B7 may prevent normal start-up.
MP1590B/MP1590B8 Network Performance Tester	➢ [CM1130:0779] ONLY install this upgrade when the MP1590B firmware is later than version 5.0 (e.g. 5.1 or later). DO NOT install this upgrade when the version is 5.0 or earlier. In this case, contact your Anritsu Service or Sales Office to upgrade the software. ➢ [CM1262:0251] The restrictions on modules for the MP1590B have changed from version 7.0. For details, see the Operation Manual. ➢ [CM1130:1300] The product key for software installer is not necessary on version 9.06.10 or later. ➢ The MP1590B8 cannot be downgraded to versions earlier than Version 10.00.00 Installing Version 9.x or earlier in the MP1590B8 may prevent normal start-up.
MD1230A Data Quality Analyzer MD1231A/31A1 IP Network Analyzer MT7407A Multislot Chassis	➢ The MD1230A/31A/31A1 and MT7407A are not supported by software version 7.0 and later. Use version 6.0.
MP1591A Network Performance Tester	➢ The MP1591A cannot use version 9.0 and later. Please use version 8.2.

Model Name	Description
MX123001A Control Software	➢ [CM1262:0646] MX123001A does not support Windows98. (Ver.7.0 or later) ➢ [CM1130:1292] MX123001A support Windows7 (Ver.9.6 or later) but following options cannot be guaranteed in Windows7. - MX123001A-06 TCL INTERFACE - MX123001A-07 RS-232C CONTROL - MX123001A-09 GPIB CONTROL - MX123001A-01 REMOTE CONTROL SOFTWARE FOR MD1230A-04 - MX123003A REMOTE CONTROL SOFTWARE FOR MX123002A
MX159001B Control Software	➢ [CM1130:1292] [CM1130:1292] MX159001B support Windows7 (Ver.9.6 or later) but MX159001B does not support 64bit version and following options cannot be guaranteed in Windows7. - MX159001B-01 RS-232C CONTROL - MX159001B-02 GPIB CONTROL
MU120118B 10 Gigabit Ethernet Module	➢ The MU120118B does not operate correctly with software versions earlier than 3.3. (With software versions 3.1 to 3.3, the MU120118B appears to be operating normally, but is actually not.) Always use software version 4.0 or later.
MU120121A 10/100/1000M Ethernet Module MU120122A Gigabit Ethernet Module	➢ MU120121A/122A: If you find following label on connector to the main frame, software version of proper operation is 5.00.27 or later. 
MU150125A 10/10.7G Jitter Unit	➢ MU150125A: If you find following label on front panel, software version of proper operation is 8.02.13 or later. 
Option 17 Traffic Impairment Emulator	➢ If the following label is not attached to the MU120121A/122A, the functions of Option 17 cannot be used (cannot switch firmware to Impairment). The label is attached to the connector that connects to the main frame. 
Option 01/02/03 Option 07/09/10 (MX123001A) RS-232C/GPIB/Ethernet Control	➢ When the software is upgraded, some remote command device messages may change. Customers using remote commands should check the MD1230A Remote Control Operation Manual when performing the version upgrade.